



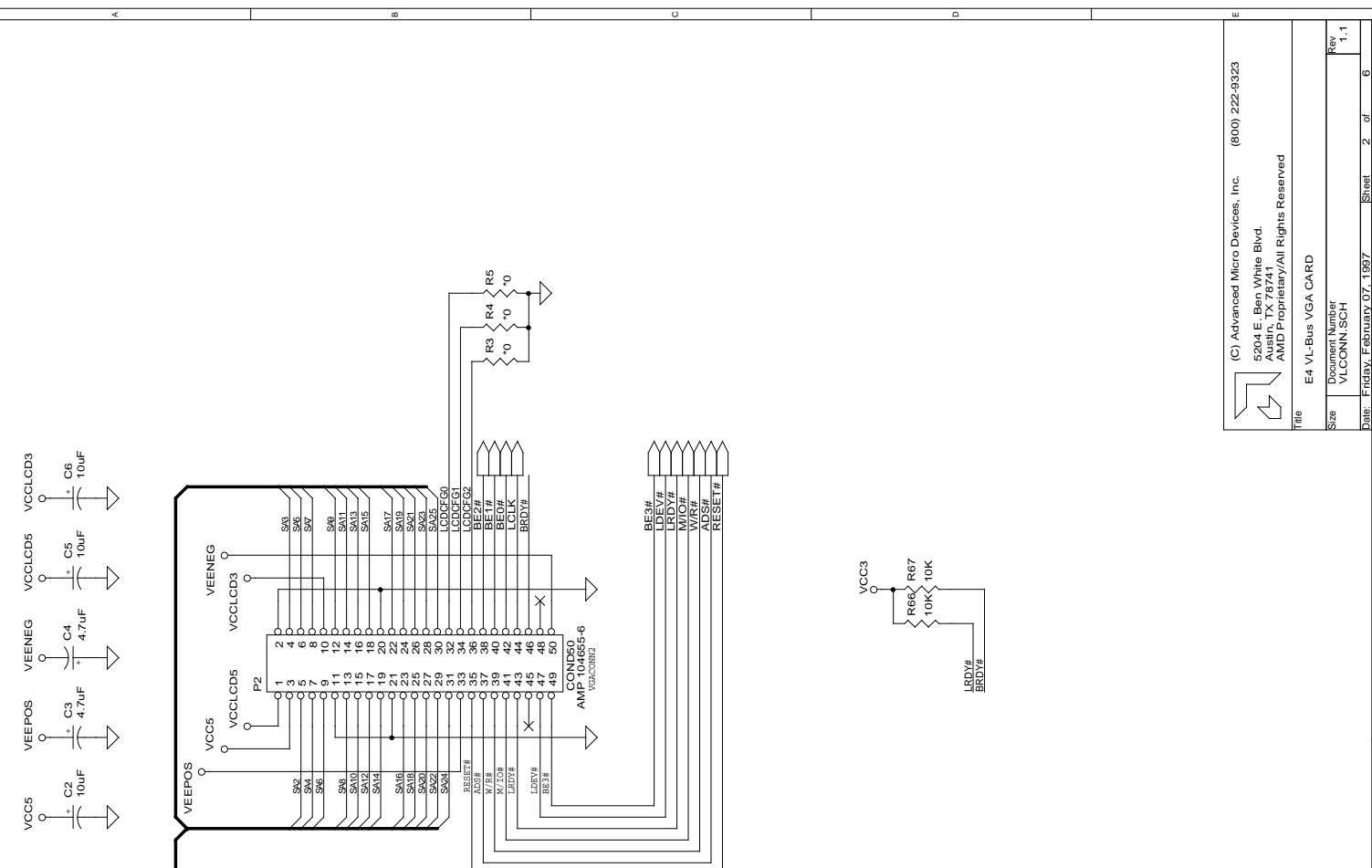
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E4 VL-Bus VGA CARD Schematics

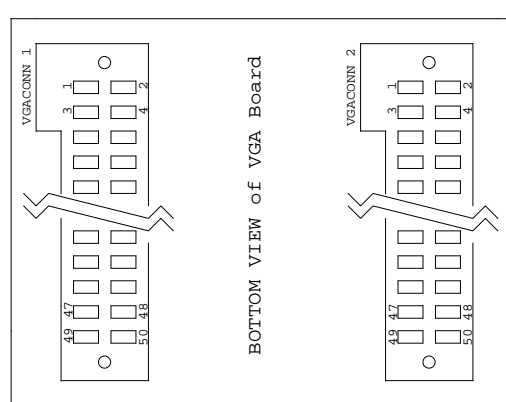
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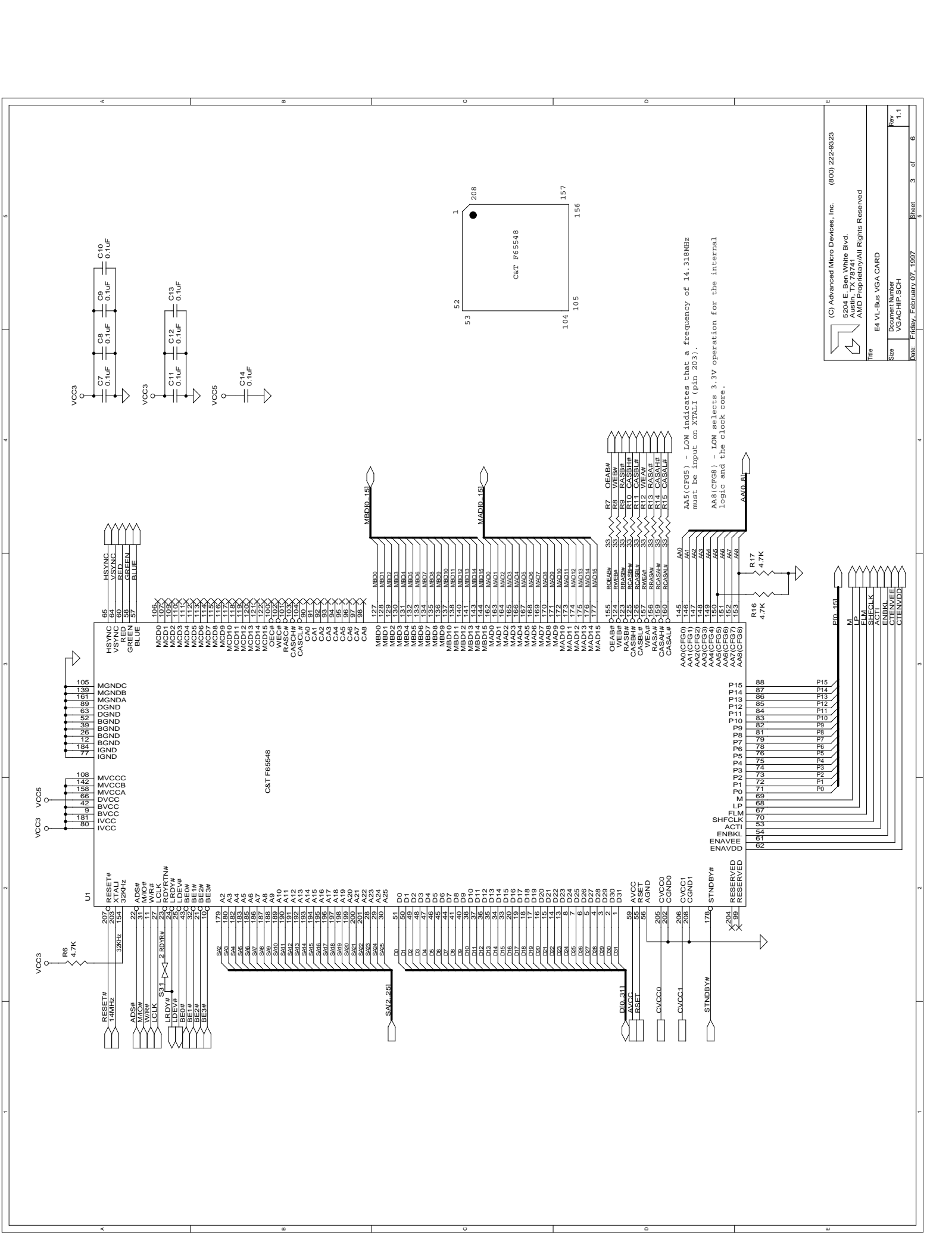
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VLCRG[1:0]:
 VLCRG2
 Identifies availability and type of VL board
 The pullup on VLCRG2 disables the VEE and VDD supplies when using the VL-Bus.

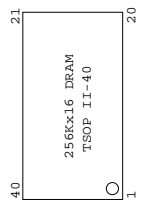
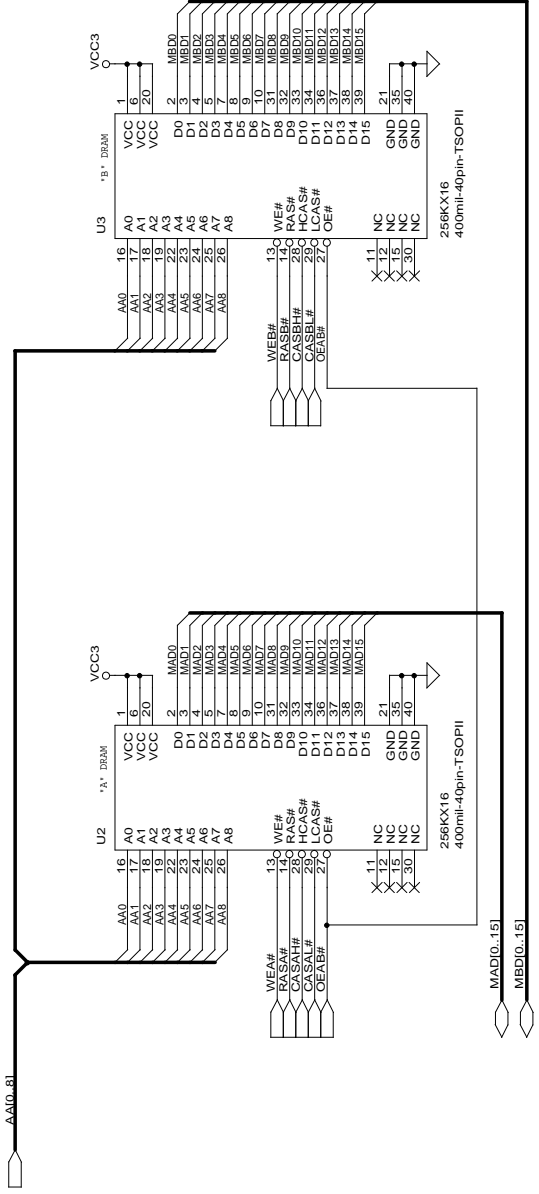
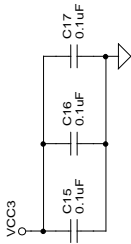
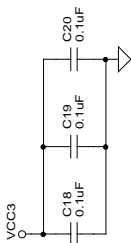
CTENVDD & CTENVEE will be used on the verification board to turn on VCCLCD & VEE when using the VL LCD.

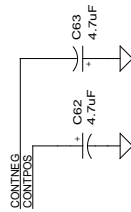
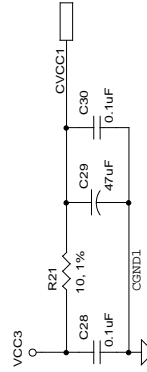
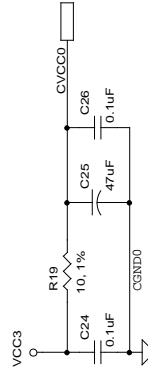
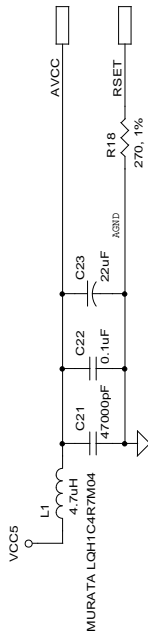
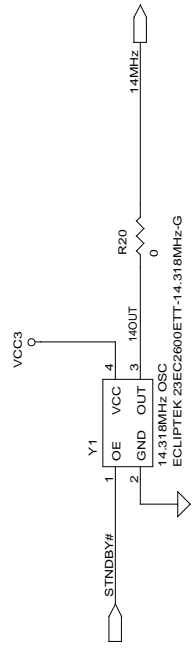
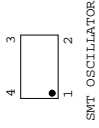




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ENBK is an enable backlight output which can be used as a gpio, or as the composite sync output.

Need to know what panels are we going to try here so I can translate P0-P15 to correct signal names.

