

;PALASM Design Description

;------ Declaration Segment -----

TITLE SMEM2.PDS
PATTERN A
REVISION 1.0
AUTHOR
COMPANY ADVANCED MICRO DEVICES
DATE 06/14/96

CHIP DECODER PALCE20V8

;------ PIN Declarations -----

| | | | |
|--------|-------|---------------|----------|
| PIN 1 | SA20 | COMBINATORIAL | ; INPUT |
| PIN 2 | SA21 | COMBINATORIAL | ; INPUT |
| PIN 3 | SA22 | COMBINATORIAL | ; INPUT |
| PIN 4 | SA23 | COMBINATORIAL | ; INPUT |
| PIN 5 | SA24 | COMBINATORIAL | ; INPUT |
| PIN 6 | SA25 | COMBINATORIAL | ; INPUT |
| PIN 7 | MEMR | COMBINATORIAL | ; INPUT |
| PIN 8 | MEMW | COMBINATORIAL | ; INPUT |
| PIN 12 | GND | | ; INPUT |
| PIN 15 | SMEMR | COMBINATORIAL | ; OUTPUT |
| PIN 16 | SMEMW | COMBINATORIAL | ; OUTPUT |
| PIN 24 | VCC | | ; INPUT |

;------ Boolean Equation Segment -----

EQUATIONS

/SMEMR = /SA20 * /SA21 * /SA22 * /SA23 * /SA24 * /SA25 * /MEMR
/SMEMW = /SA20 * /SA21 * /SA22 * /SA23 * /SA24 * /SA25 * /MEMW

;------ Simulation Segment -----

SIMULATION

TRACE_ON SA20 SA21 SA22 SA23 SA24 SA25 MEMR MEMW
SETF SA20 SA21 SA22 SA23 SA24 SA25 MEMR MEMW
CHECK SMEMR SMEMW
SETF SA20 SA21 SA22 SA23 SA24 SA25 /MEMR /MEMW
CHECK SMEMR SMEMW
SETF /SA20 /SA21 /SA22 /SA23 /SA24 /SA25 /MEMR /MEMW
CHECK /SMEMR /SMEMW
SETF /SA20 /SA21 /SA22 /SA23 /SA24 /SA25 /MEMR MEMW
CHECK /SMEMR SMEMW
SETF /SA20 /SA21 /SA22 /SA23 /SA24 /SA25 MEMR /MEMW
CHECK SMEMR /SMEMW
SETF /SA20 /SA21 /SA22 /SA23 /SA24 /SA25 MEMR MEMW
CHECK SMEMR SMEMW

TRACE_OFF

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