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TEST INTERFACE PORT

(TIP)

Schematics

REV 1.1

- Sheet 2: Removed 1K pull down on AEN.
Changed resistor values of R10 and R11.
Added a Schottky Barrier Rectifier to power supply circuit.
- Sheet 3: Added TRESET net to MACH.
- Sheet 5: Changed resistor R66 form 10K to 0 ohm.
- Sheet 6: Changed resistors R84 - R91 from 10K to 1K.
- Sheet 7: Ground AVSS1 on U26.
Added 1k pull up resistor to IOCHRDY signal.
- Sheet 8: Changed SW_IRQ# nodename to MAIN_IRQ.

REV 1.2

- Sheet 3: Replace 74HC4072 OR gate with 74HC21 AND gate.
Removed pull down resistor R15 - R18 from shared interrupt signals.
Added a pull up resistor to the PARINT signal.
- Sheet 4: Added a pull up resistor to the PEMD signal.
while making the existing pull down resistor an optional populate resistor.
- Sheet 8: Routed IOCHRDY and TIPFLHCS# nets to debug header

REV 1.3

- Sheet 3: Removed resistor R17.
Replaced AND gates with OR gates.
Made the spare MACH I/O pin 61 accessible for use.

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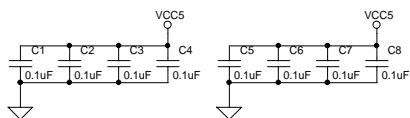
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Note: Unless otherwise stated the resistors are a 0805 package and 5% Tol.
 Note: Unless otherwise stated the capacitors are a 0805 package and 10% Tol.
 NOTE: An asterisk (*) in front of a resistor or capacitor value indicates a non populated component.

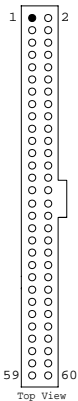
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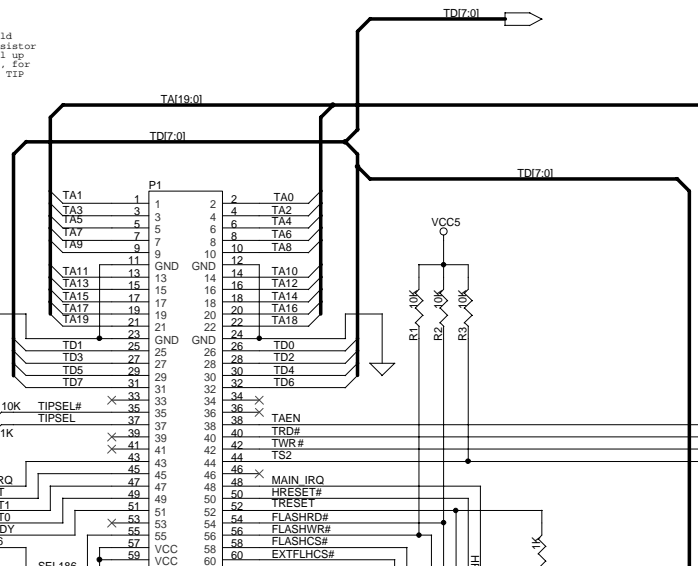
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**** NOTE ****

Target boards should use a pull down resistor on pin 35 or a pull up resistor on pin 37, for auto detect of the TIP being active

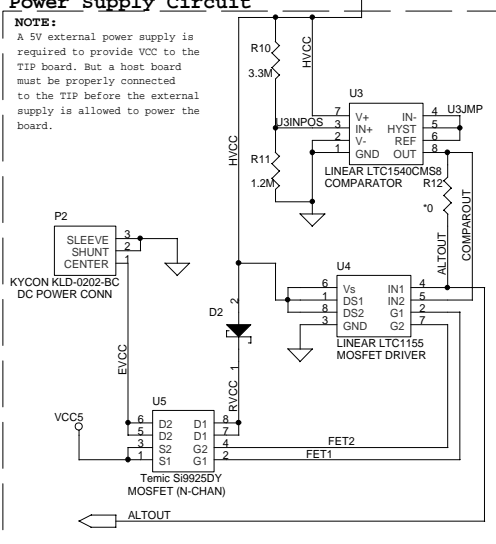


Top View



Power Supply Circuit

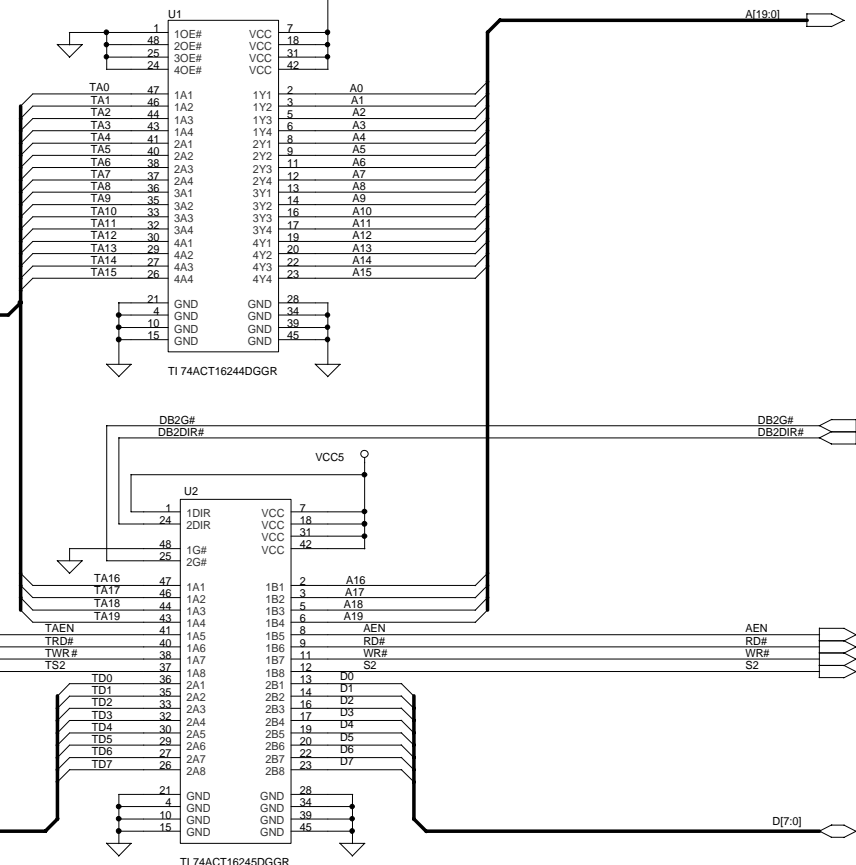
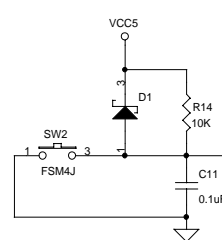
NOTE:
A 5V external power supply is required to provide VCC to the TIP board. But a host board must be properly connected to the TIP before the external supply is allowed to power the board.



Barrel Connector

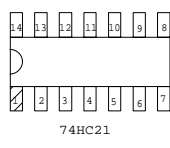
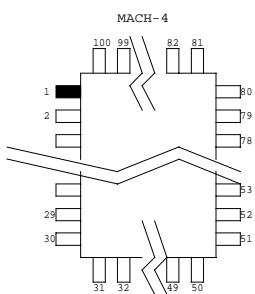
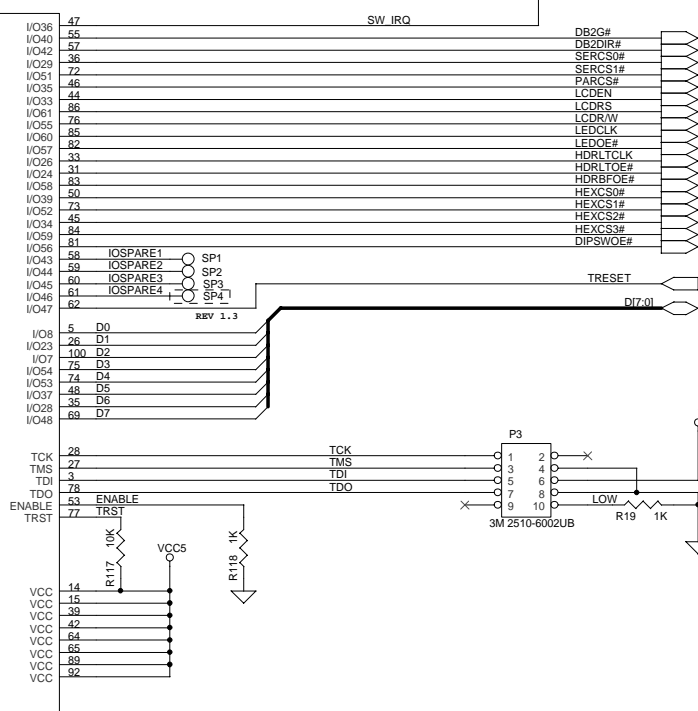
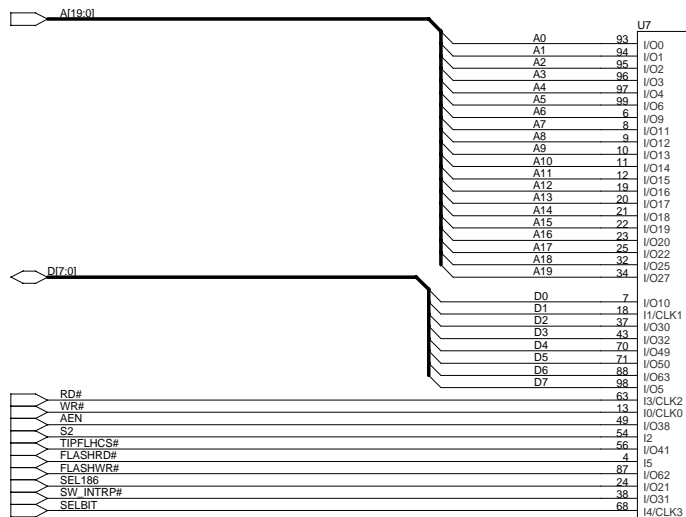
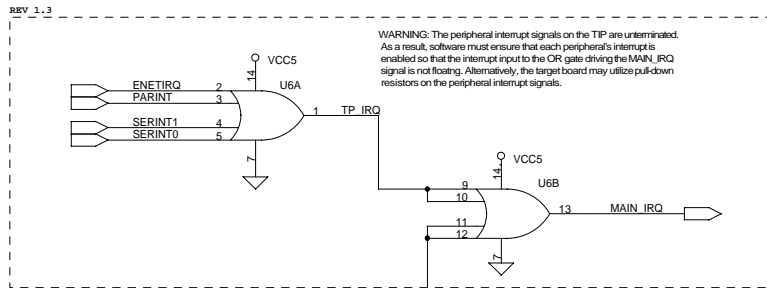
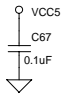
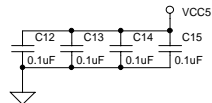
Alcoswitch FSM4J

BAT54

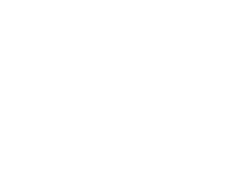
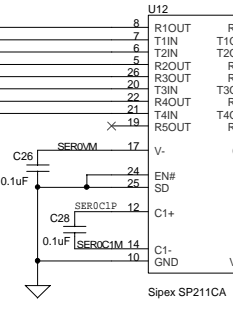
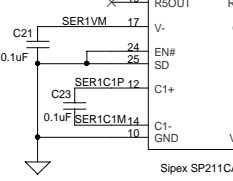
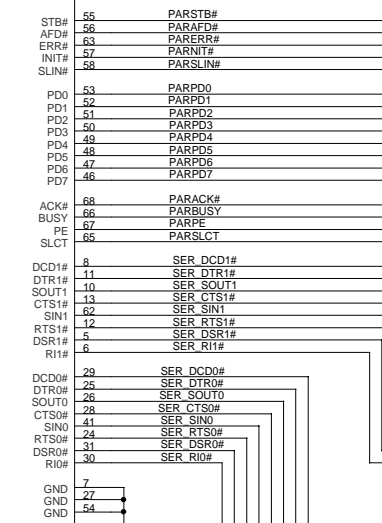
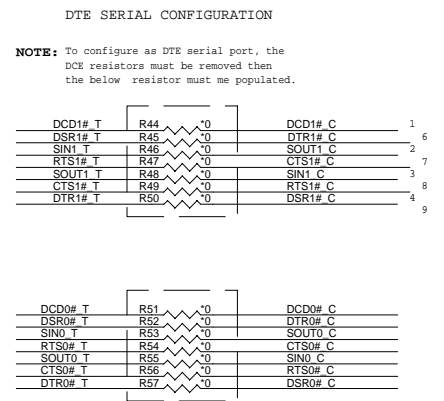
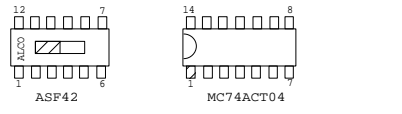
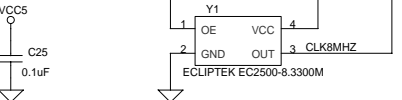
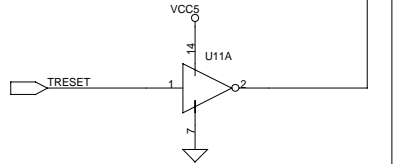
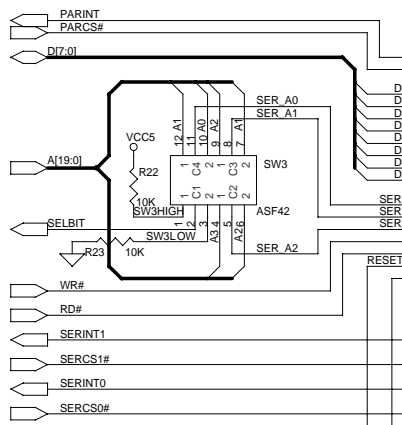


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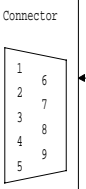
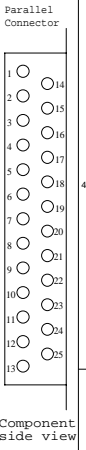
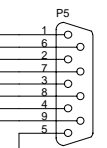
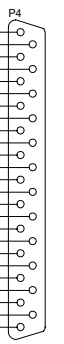


DCE SERIAL CONFIGURATION

With these resistors populated the serial port is in DCE configuration. NOTE: This is the default configuration of the TIP board.

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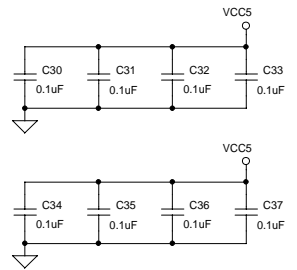
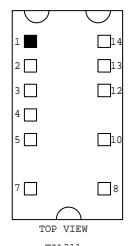
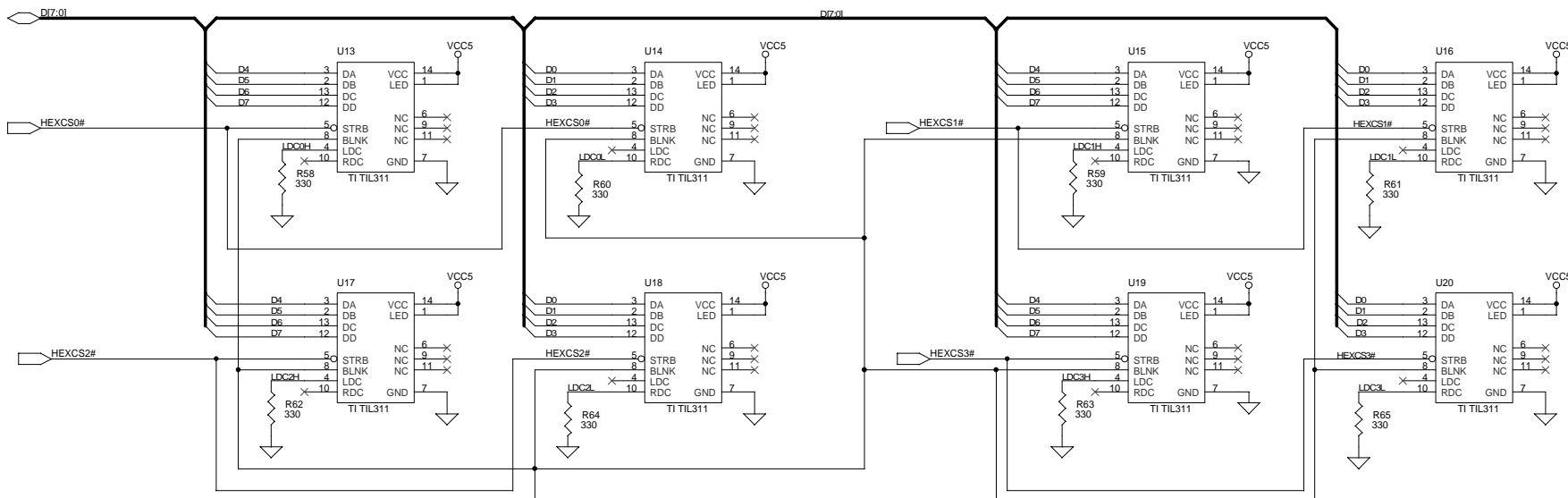


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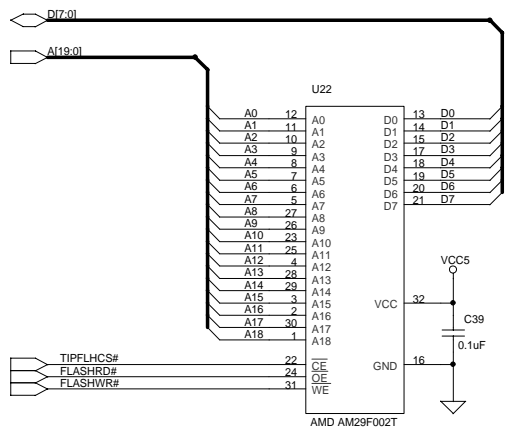
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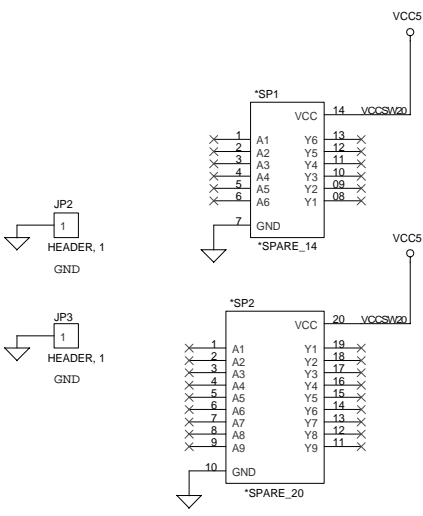
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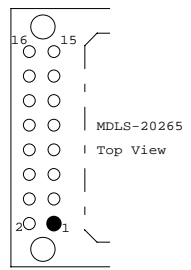
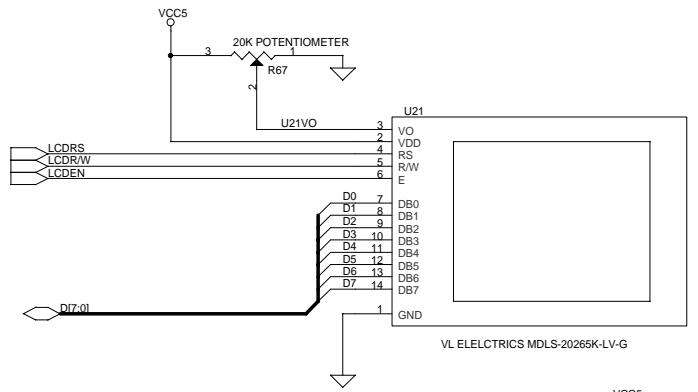
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SPARES

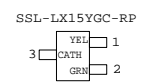
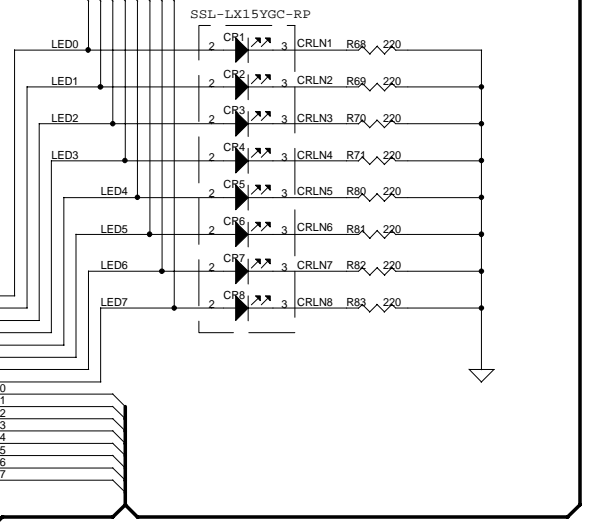
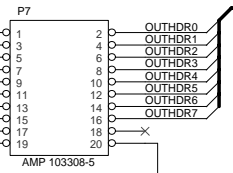
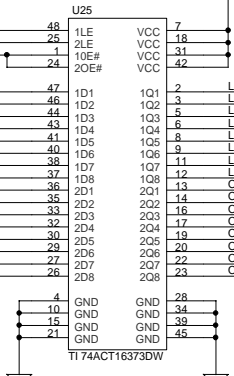
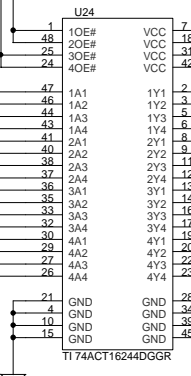
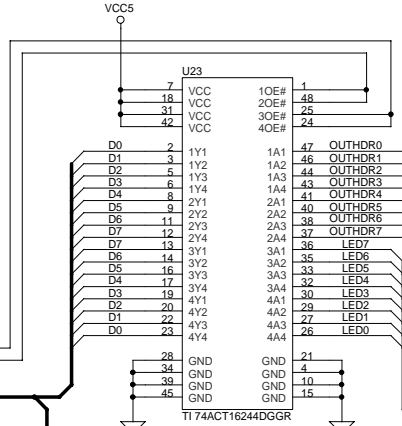
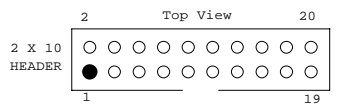
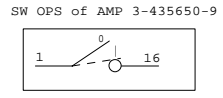
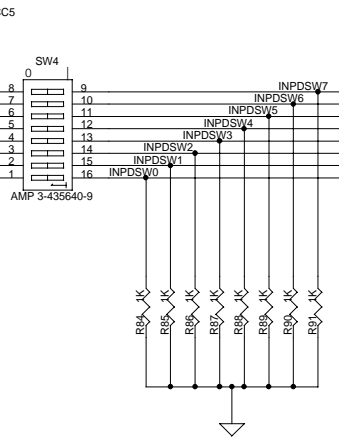
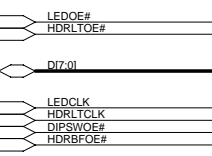
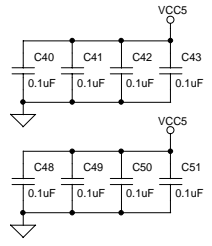


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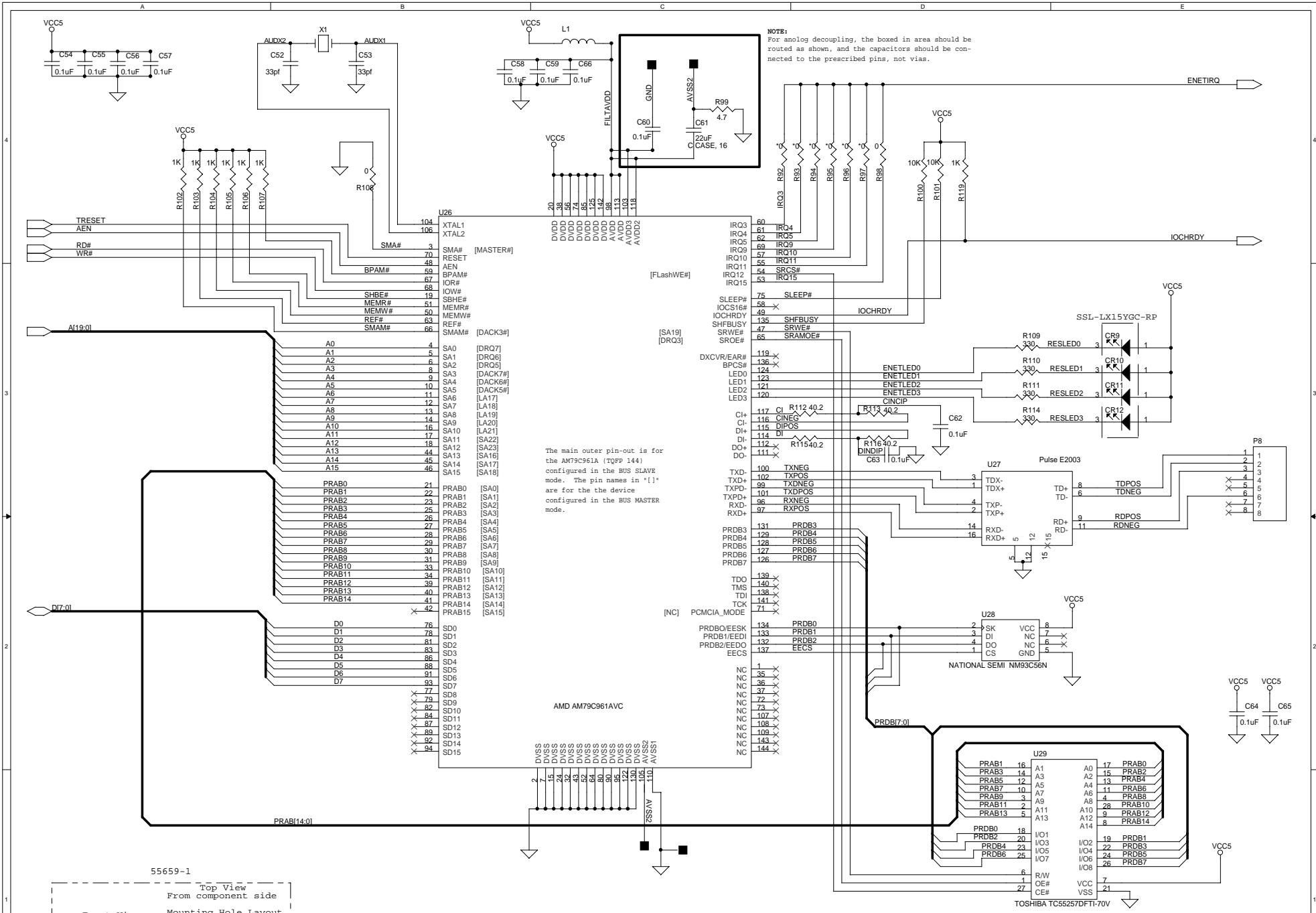


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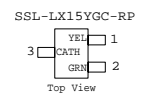
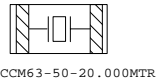
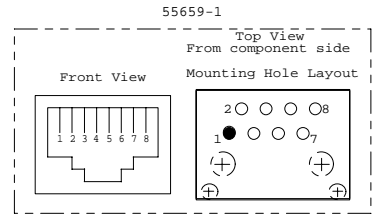
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NOTE:
For analog decoupling, the boxed in area should be routed as shown, and the capacitors should be connected to the prescribed pins, not vias.

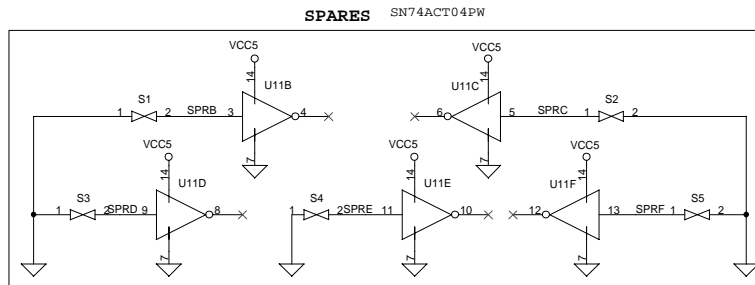
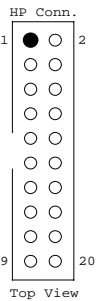
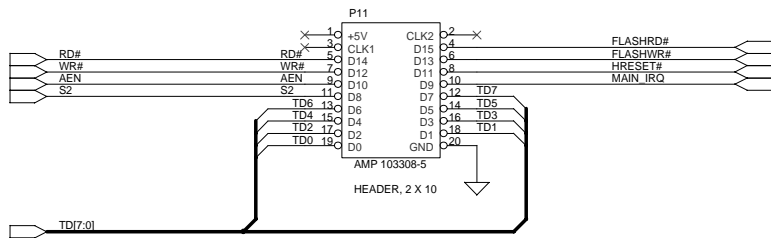
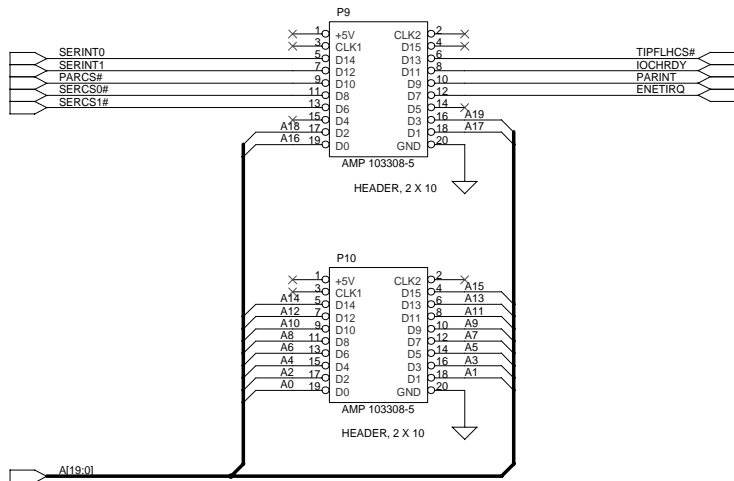
The main outer pin-out is for the AM79C961A (TQFP 144) configured in the BUS SLAVE mode. The pin names in {} are for the device configured in the BUS MASTER mode.

AMD AM79C961AVC



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