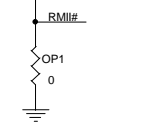


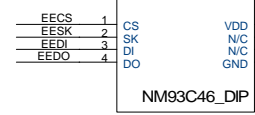
1. OP301 : Normal.
OP302 : Reduced MII.



3.3V	1	VDD
ADDR2	2	ADDR2
ADDR1	3	ADDR1
ADDR0	4	ADDR0
GND	5	Vss
CLK83M	6	CLK
RESET#	7	RESET#
GND	8	VSS
GND	9	VSS
GND	10	N/C
GND	11	N/C
CRS1	12	N/C
COL1	13	CRS0
TXEN1	14	TXE0
TXD1X_0	15	TXD0_0
TXD1X_1	16	TXD0_1
TXD1X_2	17	TXD0_2
TXD1X_3	18	TXD0_3
GND	19	VSS
TXC1	20	TXC0
RXC1	21	RXC0
RXDV1	22	RXDV0
RXD1X_0	23	RXD0_0
RXD1X_1	24	RXD0_1
RXD1X_2	25	RXD0_2
RXD1X_3	26	RXD0_3
3.3V	27	VDD
CRS2	28	CRS1
COL2	29	COL1
TXEN2	30	TXEN1
GND	31	VSS
TXD2X_0	32	TXD1_0
TXD2X_1	33	TXD1_1
TXD2X_2	34	TXD1_2
TXD2X_3	35	TXD1_3
GND	36	VSS
TXC2	37	TXC1
RXC2	38	RXC1
RXDV2	39	RXDV1
RXD2X_0	40	RXD1_0
RXD2X_1	41	RXD1_1
RXD2X_2	42	RXD1_2
RXD2X_3	43	RXD1_3
CRS3	44	CRS2
COL3	45	COL2
TXEN3	46	TXEN2
3.3V	47	VDD
TXD3X_0	48	TXD2_0
TXD3X_1	49	TXD2_1
TXD3X_2	50	TXD2_2
TXD3X_3	51	TXD2_3
3.3V	52	VDD

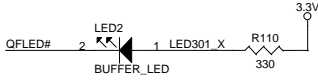
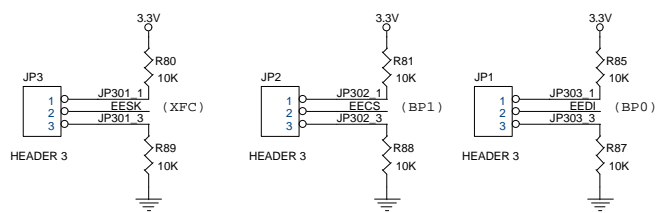
ADDR3	208	ADDR3
ADDR4	209	ADDR4
ADDR5	210	ADDR5
ADDR6	211	ADDR6
ADDR7	212	ADDR7
ADDR8	213	ADDR8
ADDR9	214	ADDR9
ADDR10	215	ADDR10
ADDR11	216	ADDR11
ADDR12	217	ADDR12
ADDR13	218	ADDR13
ADDR14	219	ADDR14
ADDR15	220	ADDR15
ADDR16	221	ADDR16
ADDR17	222	ADDR17
ADDR18	223	ADDR18
ADDR19	224	ADDR19
ADDR20	225	ADDR20
ADDR21	226	ADDR21
ADDR22	227	ADDR22
ADDR23	228	ADDR23
ADDR24	229	ADDR24
ADDR25	230	ADDR25
ADDR26	231	ADDR26
ADDR27	232	ADDR27
ADDR28	233	ADDR28
ADDR29	234	ADDR29
ADDR30	235	ADDR30
ADDR31	236	ADDR31
ADDR32	237	ADDR32
ADDR33	238	ADDR33
ADDR34	239	ADDR34
ADDR35	240	ADDR35
ADDR36	241	ADDR36
ADDR37	242	ADDR37
ADDR38	243	ADDR38
ADDR39	244	ADDR39
ADDR40	245	ADDR40
ADDR41	246	ADDR41
ADDR42	247	ADDR42
ADDR43	248	ADDR43
ADDR44	249	ADDR44
ADDR45	250	ADDR45
ADDR46	251	ADDR46
ADDR47	252	ADDR47
ADDR48	253	ADDR48
ADDR49	254	ADDR49
ADDR50	255	ADDR50
ADDR51	256	ADDR51
ADDR52	257	ADDR52
ADDR53	258	ADDR53
ADDR54	259	ADDR54
ADDR55	260	ADDR55
ADDR56	261	ADDR56
ADDR57	262	ADDR57
ADDR58	263	ADDR58
ADDR59	264	ADDR59
ADDR60	265	ADDR60
ADDR61	266	ADDR61
ADDR62	267	ADDR62
ADDR63	268	ADDR63
ADDR64	269	ADDR64
ADDR65	270	ADDR65
ADDR66	271	ADDR66
ADDR67	272	ADDR67
ADDR68	273	ADDR68
ADDR69	274	ADDR69
ADDR70	275	ADDR70
ADDR71	276	ADDR71
ADDR72	277	ADDR72
ADDR73	278	ADDR73
ADDR74	279	ADDR74
ADDR75	280	ADDR75
ADDR76	281	ADDR76
ADDR77	282	ADDR77
ADDR78	283	ADDR78
ADDR79	284	ADDR79
ADDR80	285	ADDR80
ADDR81	286	ADDR81
ADDR82	287	ADDR82
ADDR83	288	ADDR83
ADDR84	289	ADDR84
ADDR85	290	ADDR85
ADDR86	291	ADDR86
ADDR87	292	ADDR87
ADDR88	293	ADDR88
ADDR89	294	ADDR89
ADDR90	295	ADDR90
ADDR91	296	ADDR91
ADDR92	297	ADDR92
ADDR93	298	ADDR93
ADDR94	299	ADDR94
ADDR95	300	ADDR95
ADDR96	301	ADDR96
ADDR97	302	ADDR97
ADDR98	303	ADDR98
ADDR99	304	ADDR99
ADDR100	305	ADDR100

(PHOENIX)
PHOENIX_208PQFP



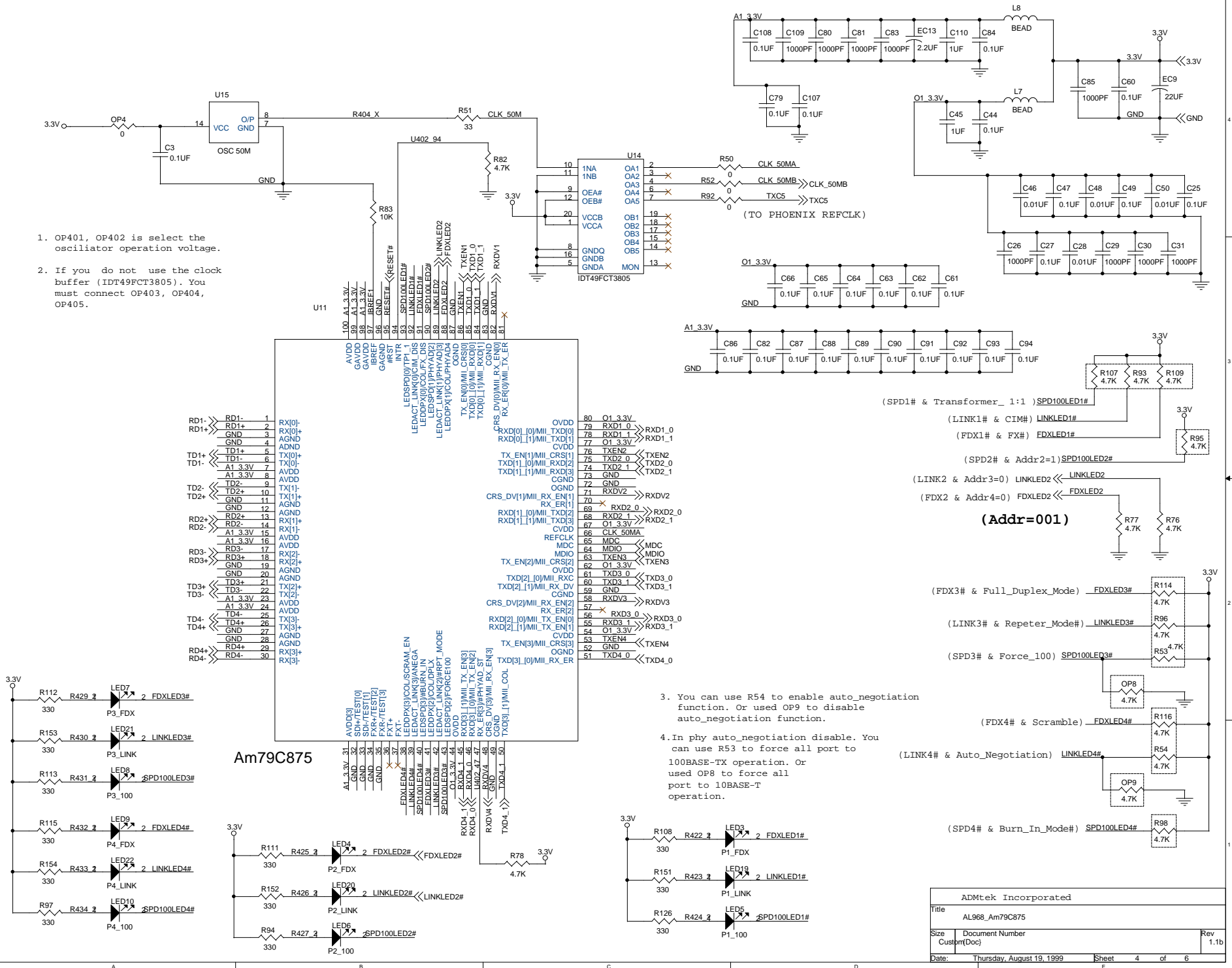
DO0	100	DO0
DO1	99	DO1
DO2	98	DO2
DO3	97	DO3
DO4	96	DO4
DO5	95	DO5
DO6	94	DO6
DO7	93	DO7
DO8	92	DO8
DO9	91	DO9
DO10	90	DO10
DO11	89	DO11
DO12	88	DO12
DO13	87	DO13
DO14	86	DO14
DO15	85	DO15
DO16	84	DO16
DO17	83	DO17
DO18	82	DO18
DO19	81	DO19
DO20	80	DO20
DO21	79	DO21
DO22	78	DO22
DO23	77	DO23
DO24	76	DO24
DO25	75	DO25
DO26	74	DO26
DO27	73	DO27
DO28	72	DO28
DO29	71	DO29
DO30	70	DO30
DO31	69	DO31
DO32	68	DO32
DO33	67	DO33
DO34	66	DO34
DO35	65	DO35
DO36	64	DO36
DO37	63	DO37
DO38	62	DO38
DO39	61	DO39
DO40	60	DO40
DO41	59	DO41
DO42	58	DO42
DO43	57	DO43
DO44	56	DO44
DO45	55	DO45
DO46	54	DO46
DO47	53	DO47
DO48	52	DO48
DO49	51	DO49
DO50	50	DO50
DO51	49	DO51
DO52	48	DO52
DO53	47	DO53
DO54	46	DO54
DO55	45	DO55
DO56	44	DO56
DO57	43	DO57
DO58	42	DO58
DO59	41	DO59
DO60	40	DO60
DO61	39	DO61
DO62	38	DO62
DO63	37	DO63
DO64	36	DO64
DO65	35	DO65
DO66	34	DO66
DO67	33	DO67
DO68	32	DO68
DO69	31	DO69
DO70	30	DO70
DO71	29	DO71
DO72	28	DO72
DO73	27	DO73
DO74	26	DO74
DO75	25	DO75
DO76	24	DO76
DO77	23	DO77
DO78	22	DO78
DO79	21	DO79
DO80	20	DO80
DO81	19	DO81
DO82	18	DO82
DO83	17	DO83
DO84	16	DO84
DO85	15	DO85
DO86	14	DO86
DO87	13	DO87
DO88	12	DO88
DO89	11	DO89
DO90	10	DO90
DO91	9	DO91
DO92	8	DO92
DO93	7	DO93
DO94	6	DO94
DO95	5	DO95
DO96	4	DO96
DO97	3	DO97
DO98	2	DO98
DO99	1	DO99

SGRAM PIN 30 CONNECT TO GND THAT THE CIRCUIT CAN USE BOTH 256Kx32 AND 512Kx32 SGRAM.



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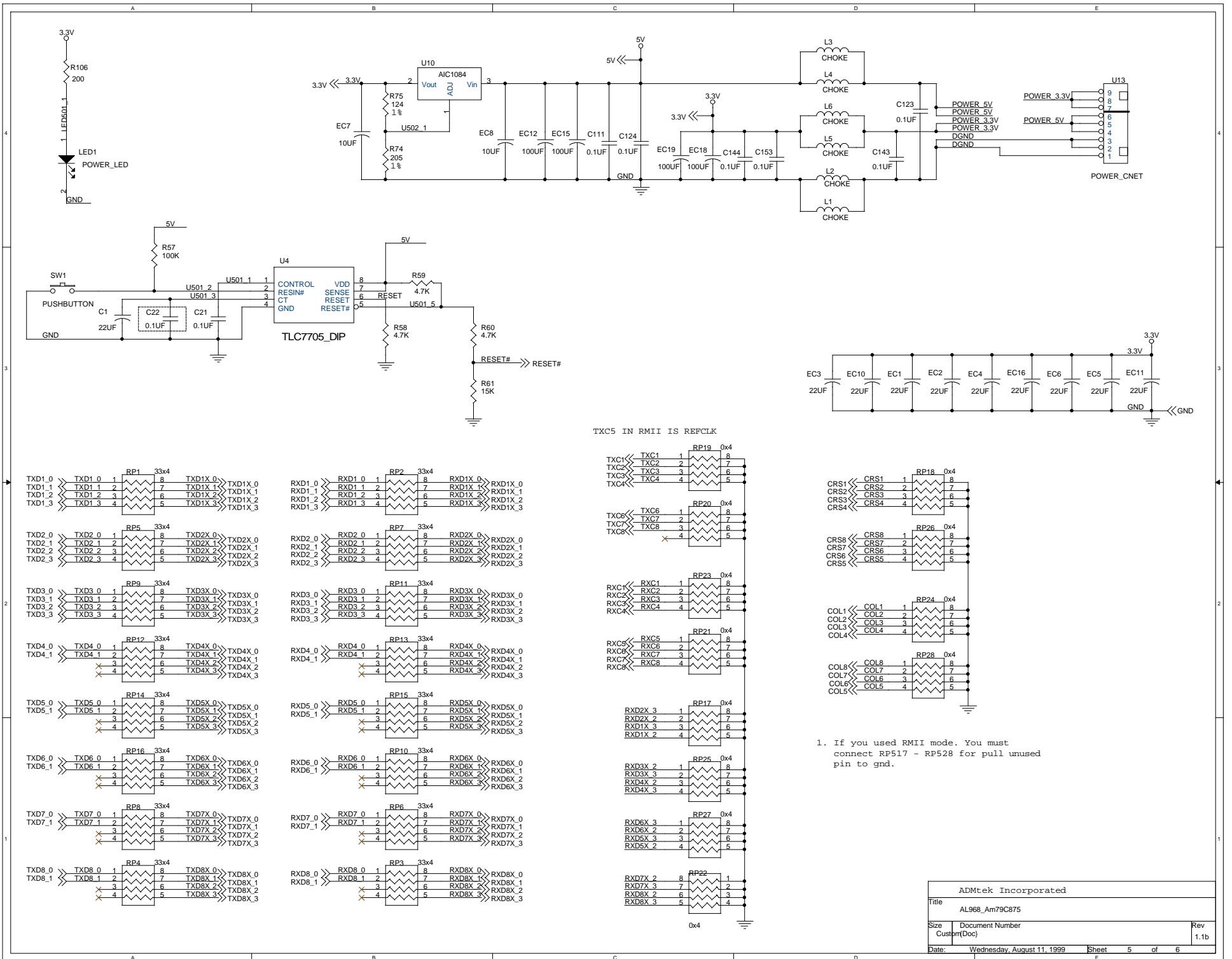
- OP401, OP402 is select the oscillator operation voltage.
- If you do not use the clock buffer (IDT49FCT3805). You must connect OP403, OP404, OP405.



Am79C875

- You can use R54 to enable auto_negotiation function. Or used OP9 to disable auto_negotiation function.
- In phy auto_negotiation disable. You can use R53 to force all port to 100BASE-TX operation. Or used OP8 to force all port to 10BASE-T operation.

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Date:	Thursday, August 19, 1999	Sheet 4 of 6



TXC5 IN RMII IS REFLCK

1. If you used RMII mode. You must connect RP517 - RP528 for pull unused pin to gnd.

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Custom(Doc)	Rev
Date: Wednesday, August 11, 1999	Sheet 5 of 6
	1.1b

