

AMD-8131™ HyperTransport™ PCI-X Tunnel

AMD introduces the AMD-8131™ HyperTransport™ PCI-X tunnel. This high-speed device provides two independent, high-performance PCI-X bus bridges, integrated with a high-speed HyperTransport technology tunnel. The tunnel function provides connection capability to other downstream HyperTransport technology devices, allowing greater system flexibility.

As shown in Figure 1, the front-end HyperTransport interface (Side A) provides a configurable 16-bit wide communication path to the host, offering up to 6.4GB/s of bandwidth. Similarly, the back-end HyperTransport interface (Side B) provides a configurable 8-bit wide communication path to a downstream device, offering up to 3.2GB/s of bandwidth.

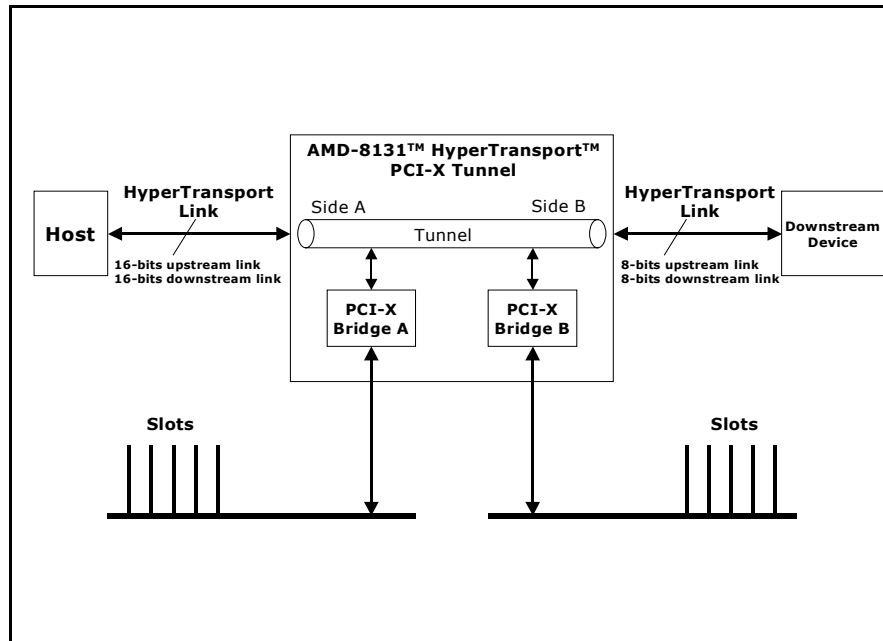


Figure 1: AMD-8131™ HyperTransport™ PCI-X tunnel

Features

Component features are as follows:

- 16-bit HyperTransport interface (Side A) offering a maximum bandwidth of up to 6.4GB/s
- 8-bit HyperTransport interface (Side B) offering a maximum bandwidth of up to 3.2GB/s
- Two PCI-X bridges (Bridge A and Bridge B) supporting the following features:
 - PCI-X modes and legacy PCI revision 2.2 modes
 - 133MHz, 100MHz, 66MHz, and 33MHz transfer rates in PCI-X mode
 - 66MHz and 33MHz PCI 2.2 modes
 - Independent transfer rates and operational modes for each bridge
 - Each bridge includes support for up to five PCI masters
 - Each bridge includes an IOAPIC with four redirection registers – legacy interrupt controller and IOAPIC modes supported
 - SHPC-compliant hot plug controller and support
 - 829-pin OBGA package

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