AMD EPYC™ 9xx4-series Processors

Compiler Options Quick Reference Guide



AOCC compiler (C/C++/Fortran)

Latest release: 4.1, August 2023

https://www.amd.com/en/developer/aocc.html

Architecture		Other options	
			ffeet meth
Generate instructions that runs on AMD 4^{th} Gen EPYC TM and AMD 4^{th}	-march=znver4	Enables faster, less precise math operations (part of Ofast)	-ffast-math -freciprocal-math
Gen Ryzen [™]		OpenMP® threads and affinity	export OMP_NUM_THREADS=N
Generate instructions supported in the given machine	-march=native		export GOMP_CPU_AFFINITY="0- {N-1}"
Optimization Levels		Link to AMD library	-L/libm-install-dir/lib -lamdlibm -lm
Disables all optimizations	-00	Enables vector library	-lamdlibm -fveclib=AMDLIBM -lm
Enables minimal level optimizations	-01	Enables faster library	-lamdlibm -fscIrlib=AMDLIBM - lamdlibmfast -lm
Enables moderate level optimiza-	-02/-0	For Fortran Workloads	
tions (Default from AOCC 4.1)		Compiles Fortran free form -ffr	-ffree-form
Enables all optimizations that	-03		
attempt to make programs run faster			
	Ofest	AMD Optimized Librari	es
Enables O3 with other aggressive optimizations that may violate	-Ofast	Latest release: 4.1, August 2023 https://www.amd.com/en/developer/aocl.html	
strict compliance and precisions			eloper/aocl.html
Enables link time optimization	-flto	AMD µProf (Performance & Power Profiler)	
More advanced optimizations -	-zopt	Latest release: 4.1 August 2023	
Enables improved variants of vari-		https://www.amd.com/en/deve	
ous scalar, vector and loop trans- formations			
Enables advanced vector transfor-	-fvector-transform		
mations	-mllvm -enable-strided-		
	vectorization		
Enables loop transformations	-floop-transform		
Enables advanced loop transfor-	-faggressive-loop-transform		
mations			
Enables memory layout optimiza-	-flto -fremap-arrays		
tions	-mllvm -reduce-array-		
	computations=3		
Enables function level optimizations	-fito -fitodcalls -mllvm -function-specialize		
LIONS	-filto -finline-recursion={14}		
	(2)		
Profile guided optimizations	-fprofile-instr-generate (1st invocation)		
	-fprofile-instr-use (2nd invocation)		
Enables use of OpenMP® direc-	-fopenmp		
tives	· openinp		
Enables streaming stores to opti-	-fnt-store		
3			

mize memory bandwidth usage

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GNU compiler collection

Latest release: GCC 13.2, July 2023 Recommended version: GCC 13.1 or later

http://gcc.gnu.org

Microsoft® Visual Studio 2022

Latest release: 17.6.3, June 2023 https://visualstudio.microsoft.com/

User Guide

Architecture		Architecture	
Generate instructions that runs on AMD 4 th Gen EPYC [™] and AMD 4 th Gen Ryzen [™]	-march=znver4	Generate instructions that runs on AMD 4 th Gen EPYC [™] and AMD 4 th Gen Ryzen [™]	/arch:[AVX AVX2 AVX512]
Generate instructions supported in the given machine	-march=native	Optimize for 64-bit AMD processors	/favor:AMD64
Optimization Levels		Optimization Levels	
Disables all optimizations (default)	-00	Disable optimizations	/Od
Enables minimal level optimizations	-01/-0	Maximum optimizations (favor /O1 [includes /Ob2]	/O1 [includes /Ob2]
Enables moderate level optimizations	-02	space)	/ /
Enables all optimizations that attempt to make programs run faster	-03	Maximum optimizations (favor speed)	/O2 [includes /Ob2]
	-Ofast	Enables inline expansion	/Ob (0/1/2/3)
Enables O3 with other aggressive optimizations that may violate strict compliance and precisions	-Ulast	[link.exe] Eliminates unreferenced function and/ or data	/OPT:REF
Additional Optimizations		[link.exe] Performs identical	/OPT:ICF
Enables link time optimizations	-flto	COMDAT folding Output an informational message /Qvec-report:[1 2] for loops that are auto-vectorized	
Enables unrolling	-funroll-all-loops		/Qvec-report:[1 2]
Generates memory preload instructions	-fprefetch-loop-arrays	Enables automatic parallelization of /Qpar loops, used in conjunction with #pragma loop() directive	/Qpar
Enables profile-guided optimizations	-fprofile-generate (1st invocation) -fprofile-use (2nd invocation)		
Enables use of OpenMP® directives	-fopenmp	Output an informational message	/Qpar-report:[1 2]
Other options		for loops that are auto-parallelized	
Enables compiler to use IEEE FP compari-	-mieee-fp	Additional Optimizations	
sons		Maintain the precision for floating- point operations through proper	/fp:precise
Enables faster, less precise math operations	-ffast-math	rounding	
Compiles Fortran free form layout	-ffree-form	Optimize floating-point code for /fp speed at the expense of floating-	/fp:fast
OpenMP® threads and affinity (N num-	export OMP_NUM_THREADS=N	Point accuracy and correctness	
ber of cores)	export GOMP_CPU_AFFINITY="0-{N -1}"		/GL
Link to AMD library	-L/libm-install-dir/lib -lamdlibm -lm	Enables Profile-guided optimizations	LTCG:PGI and /LTCG:PGO
		Enables OpenMP Support	/openmp:experimental /openmp:llvm

GlibC

Binutils Latest release: 2.38, July 2023

Recommendation: 2.38 or later https://www.gnu.org/software/libc/ Latest release: 2.41, July 2023 Recommendation: 2.40 or later

https://www.gnu.org/software/binutils/

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Intel® oneAPI DPC++/C++ Compiler

Latest release: 2023.1.0 http://software.intel.com

Architecture				
Generate instructions that runs on AMD 4^{th} Gen EPYC TM and AMD 4^{th} Gen Ryzen TM	-axCORE-AVX512			
Optimization Levels				
Disable all optimizations	-00			
Speed optimization without code growth	-01			
Enables optimization for speed including vectorization	-02			
Enables O2 and aggressive loop	-03			
Enables set of aggressive options to	-Ofast			
Additional Optimizations				
Sets function inline level	-inline-level= <value></value>			
Sets unroll loop maximum threshold	-unroll <value></value>			
Disable improved precision floating divides	-no-prec-div			
Enables vectorization	-vec			
Enables inter procedural optimiza-	-ipo			
Enables use of OpenMP® directives	-qopenmp			
Enables profile generated optimiza-	-prof-gen and -prof-use			
Other Options				
Enables floating point accuracy tun-	-fp-model			
Compiles Fortran free form layout	-free			

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